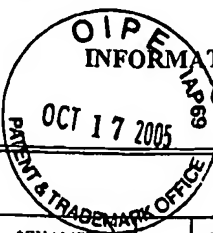
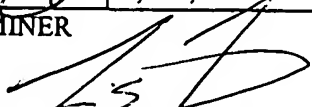
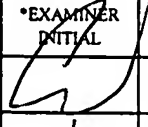
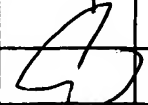


FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501328.01 (30313/US)		APPLICATION NO. 10/695,383	
				APPLICANT(S) Ralph James		GROUP ART UNIT 2186	
				FILING DATE October 27, 2003			
<b>U.S. PATENT DOCUMENTS</b>							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	3,742,253	06/26/73	Kronies	307	247	
	AB	4,608,702	08/26/86	Hirzel et al.	375	110	
	AC	4,697,167	09/29/87	O'Keeffe et al.	340	347	
	AD	4,740,962	04/26/88	Kish, III	370	102	
	AE	4,791,622	12/13/88	Clay et al.	369	59	
	AF	4,831,520	05/16/89	Rubinfeld et al.	364	200	
	AG	4,891,808	01/02/90	Williams	370	112	
	AH	4,924,516	05/08/90	Bremer et al.	380	46	
	AI	5,313,590	05/17/94	Taylor	395	325	
	AJ	5,317,752	05/31/94	Jewett et al.	395	750	
	AK	5,327,553	07/05/94	Jewett et al.	395	575	
	AL	5,638,534	06/10/97	Mote, Jr.	395	485	
	AM	5,687,325	11/11/97	Chang	395	284	
	AN	5,706,224	01/06/98	Srinivasan et al.	365	49	
	AO	5,710,733	01/20/98	Chengson et al.	365	52	
	AP	5,748,616	05/05/98	Riley	370	242	
	AQ	5,796,413	08/18/98	Shipp et al.	345	522	
	AR	5,887,159	03/23/99	Burrows	395	567	
	AS	6,038,630	03/14/00	Foster et al.	710	132	
	AT	6,067,649	05/23/00	Goodwin	714	718	
	AU	6,072,802	06/06/00	Uhm et al.	370	441	
	AV	6,092,158	07/18/00	Harriman et al.	711	151	
	AW	6,128,703	10/03/00	Bourekas et al.	711	138	
	AX	6,131,149	10/10/00	Lu et al.	711	167	
	AY	6,185,676 B1	02/06/01	Poplingher et al.	712	239	
EXAMINER 				DATE CONSIDERED 12/8/05			
<p>* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).</p>							

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501328.01 (30313/US)	APPLICATION NO. 10/695,383
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) Ralph James	
		FILING DATE October 27, 2003	GROUP ART UNIT 2186

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AZ	6,208,180 B1	03/27/01	Fisch et al.	327	141	
	BA	6,219,725 B1	04/17/01	Diehl et al.	710	26	
	BB	6,223,301 B1	04/24/01	Santeler et al.	714	6	
	BC	6,286,083 B1	09/04/01	Chin et al.	711	151	
	BD	6,370,611 B1	04/09/02	Callison et al.	711	105	
	BE	6,389,514 B1	05/14/02	Rokicki	711	136	
	BF	6,433,785 B1	08/13/02	Garcia et al.	345	531	
	BG	6,460,114 B1	10/01/02	Jeddeloh	711	120	
	BH	6,487,556 B1	11/26/02	Downs et al.	707	101	
	BI	6,493,803 B1	12/10/02	Pham et al.	711	147	
	BJ	6,496,193 B1	12/17/02	Surti et al.	345	552	
	BK	6,526,483 B1	02/25/03	Cho et al.	711	154	
	BL	6,564,329 B1	05/13/03	Cheung et al.	713	322	
	BM	6,647,470 B1	11/11/03	Janzen	711	154	
	BN	6,665,202 B2	12/16/03	Lindahl et al.	365	49	
	BO	6,785,780 B1	08/31/04	Klein et al.	711	148	
	BP	6,889,304 B2	05/03/05	Perego et al.	711	170	
	BQ	6,910,109 B2	06/21/05	Holman et al.	711	156	
	BR	2002/0178319 A1	11/28/02	Sanchez-Olea	710	305	
	BS	2003/0217223 A1	11/20/03	Nino, Jr., et al.	711	105	
	BT	2003/0227798 A1	12/11/03	Pax	365	189.12	
	BU	2003/0229762 A1	12/11/03	Maiyuran et al.	711	137	
	BV	2004/0044833 A1	03/04/04	Ryan	711	5	
	BW	2004/0064602 A1	04/01/04	George	710	22	
	BX	2005/0044327 A1	02/24/05	Howard et al.	711	147	

EXAMINER

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FORM PTO-1449  
(REV.7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
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## INFORMATION DISCLOSURE STATEMENT

(Use separate sheets if necessary)

APPLICANT(S)  
Ralph JamesFILING DATE  
October 27, 2003GROUP ART UNIT  
2186

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	6,119,242	09/12/00	Harrison	713	503	
AB	6,845,409 B1	01/18/05	Talagala et al.	710	20	
AC	2003/0149809 A1	08/07/03	Jensen et al.	710	22	
AD	2004/0236885 A1	11/25/04	Fredriksson et al.	710	100	
AE						
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## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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FORM PTO-1449  
(REV. 7-80)INFORMATION DISCLOSURE STATEMENT  
(Include copies of documents if necessary)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
501328.01APPLICATION NO.  
10/695,383APPLICANT(S)  
Ralph JamesFILING DATE  
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## U.S. PATENT DOCUMENTS

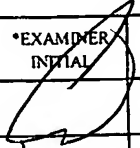
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AA	4,045,781	08/30/77	Levy et al.	364	200	
AB	4,240,143	12/16/80	Besemer et al.	364	200	
AC	4,245,306	01/13/81	Besemer et al.	364	200	
AD	4,253,144	02/24/81	Bellamy et al.	364	200	
AE	4,253,146	02/24/81	Bellamy et al.	364	200	
AG	4,724,520	02/09/88	Athanas et al.	364	200	
AG	4,930,128	05/29/90	Suzuki et al.	371	12	
AH	4,953,930	09/04/90	Ramsey et al.	350	96.11	
AI	5,269,022	12/07/93	Shinjo et al.	395	700	
AJ	5,317,752	05/31/94	Jewett et al.	395	750	
AK	5,461,627	10/24/95	Rypinski	370	95.2	
AL	5,479,370	12/26/95	Furuyama et al.	365	189.12	
AM	5,577,220	11/19/96	Combs et al.	395	416	
AN	5,715,456	02/03/98	Bennett et al.	395	652	
AO	5,729,709	03/17/98	Harness	395	405	
AP	5,832,250	11/03/98	Whittaker	395	471	
AQ	5,966,724	10/12/99	Ryan	711	105	
AR	5,978,567	11/02/99	Rebane et al.	395	200.49	
AS	5,987,196	11/16/99	Noble	385	14	
AT	6,023,726	02/08/00	Saksena	709	219	
AU	6,031,241	02/29/00	Silfvast et al.	250	504 R	
EXAMINER			DATE CONSIDERED			
			12-8-05			

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FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. <b>501328.01</b>	APPLICATION NO. <b>10/695,383</b>
		APPLICANT(S) <b>Ralph James</b>	
		FILING DATE <b>October 27, 2003</b>	GROUP ART UNIT <b>2186</b>

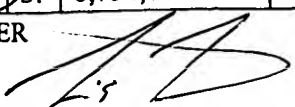
**INFORMATION DISCLOSURE STATEMENT**  
(Use several sheets if necessary)

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AV	6,076,139	06/13/00	Welker et al.	711	104	
	AW	6,098,158	08/01/00	Lay et al.	711	162	
	AX	6,134,624	10/17/00	Burns et al.	710	131	
	AY	6,175,571 B1	01/16/01	Haddock et al.	370	423	
	AZ	6,191,663 B1	02/20/01	Hannah	333	17.3	
	BA	6,249,802 B1	06/19/01	Richardson et al.	709	200	
	BB	6,405,280 B1	06/11/02	Ryan	711	105	
	BC	6,434,696 B1	08/13/02	Kang	713	2	
	BD	6,438,622 B1	08/20/02	Haghighi et al.	710	1	
	BE	6,449,308 B1	09/10/02	Knight, Jr. et al.	375	212	
	BF	6,453,393 B1	09/17/02	Holman et al.	711	154	
	BG	6,501,471 B1	12/31/02	Venkataraman et al.	345	424	
	BH	6,594,722 B1	07/15/03	Willke, II et al.	710	313	
	BI	6,598,154 B1	07/22/03	Vaid et al.	712	237	
	BJ	6,615,325 B2	09/02/03	Mailloux et al.	711	154	
	BK	6,628,294 B1	09/30/03	Sadowsky et al.	345	568	
	BL	6,629,220 B1	09/30/03	Dyer	711	158	
	BM	6,658,509 B1	12/02/03	Bonella et al.	710	100	
	BN	6,697,926 B2	02/24/04	Johnson et al.	711	167	
	BO	6,715,018 B2	03/30/04	Farnworth et al.	710	300	
	BP	6,718,440 B2	04/06/04	Maiyuran et al.	711	137	
	BQ	6,728,800 B1	04/27/04	Lee et al.	710	54	
	BR	6,735,679 B1	05/11/04	Herbst et al.	711	167	
	BS	6,735,682 B2	05/11/04	Segelken et al.	711	220	
	BT	6,754,812 B1	06/22/04	Abdallah et al.	712	234	

EXAMINER

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FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501328.01	APPLICATION NO. 10/695,383
		APPLICANT(S) Ralph James	
		FILING DATE October 27, 2003	GROUP ART UNIT 2186

# INFORMATION DISCLOSURE STATEMENT

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## U.S. PATENT DOCUMENTS

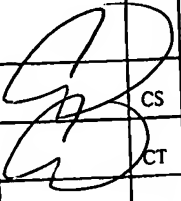
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BU	6,760,833 B1	07/06/04	Dowling	712	34	
BV	6,775,747 B2	08/10/04	Venkatraman	711	137	
BW	6,789,173 B1	09/07/04	Tanaka et al.	711	147	
BX	6,792,496 B2	09/14/04	Aboulenein et al.	710	306	
BY	6,795,899 B2	09/21/04	Dodd et al.	711	137	
BZ	6,799,268 B1	09/28/04	Boggs et al.	712	228	
CA	6,804,760 B2	10/12/04	Williams	711	170	
CB	6,807,630 B2	10/19/04	Lay et al.	713	2	
CC	6,811,320 B1	11/02/04	Abbott	385	58	
CD	6,816,947 B1	11/09/04	Huffman	711	151	
CE	6,820,181 B2	11/16/04	Jeddeloh et al.	711	169	
CF	6,821,029 B1	11/23/04	Grung et al.	385	92	
CG	6,823,023 B1	11/23/04	Hannah	375	296	
CH	2001/0039612 A1	11/08/01	Lee	713	2	
CI	2002/0116588 A1	08/22/02	Beckert et al.	711	161	
CJ	2003/0043158 A1	03/06/03	Wasserman et al.	345	545	
CK	2003/0093630 A1	05/15/03	Richard et al.	711	154	
CL	2003/0163649 A1	08/28/03	Kapur et al.	711	146	
CM	2003/0177320 A1	09/18/03	Sah et al.	711	158	
CN	2003/0193927 A1	10/16/03	Hronik	370	351	
CO	2004/0022094 A1	02/05/04	Radhakrishnan et al.	365	200	
CP	2004/0126115 A1	07/01/04	Levy et al.	398	116	
CQ	2004/0144994 A1	07/29/04	Lee et al.	257	200	
CR	2005/0071542 A1	03/31/05	Weber et al.	711	105	

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(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
501328.01APPLICATION NO.  
10/695,383**INFORMATION DISCLOSURE STATEMENT**  
(Use several sheets if necessary)APPLICANT(S)  
**Ralph James**FILING DATE  
October 27, 2003GROUP ART UNIT  
2186**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	CS	2001265539 A	09/28/01	Japan (+ English Abstract)			X	
	CT	WO 02/27499 A2	04/04/02	PCT				
	CU							

**OTHER PRIOR ART** (Including Author, Title, Date, Pertinent Pages, Etc.)

Intel, "Flash Memory PCI Add-In Card for Embedded Systems", Application Note AP-758, September, 1997, pp. i-13.

Shanley, T. et al., "PCI System Architecture", Third Edition, Mindshare, Inc., 1995, pp. 24-25.

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APPLICANTS  
Ralph JamesFILING DATE  
October 27, 2003GROUP ART UNIT  
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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	3,633,174	01/04/72	Griffin	340	172.5	
AB	4,004,100	01/18/77	Takimoto	179	15 BS	
AC	4,077,016	02/28/78	Sanders et al.	331	4	
AD	4,096,402	06/20/78	Schroeder et al.	307	362	
AE	4,404,474	09/13/83	Dingwall	307	260	
AF	4,481,625	11/06/84	Roberts et al.	370	85	
AG	4,508,983	04/02/85	Allgood et al.	307	577	
AH	4,511,846	04/16/85	Nagy et al.	328	164	
AI	4,514,647	04/30/85	Shoji	307	269	
AJ	4,524,448	06/18/85	Hullwegen	375	118	
AK	4,573,017	02/25/86	Levine	327	114	
AL	4,600,895	07/15/86	Landsman	331	1 A	
AM	4,603,320	07/29/86	Farago	341	89	
AN	4,638,187	01/20/87	Boler et al.	307	451	
AO	4,638,451	01/20/87	Hester et al.	395	889	
AP	4,687,951	08/18/87	McElroy	307	269	
AQ	4,727,541	02/23/88	Mori et al.	370	112	
AR	4,746,996	05/24/88	Furuhata et al.	360	36.2	
AS	4,773,085	09/20/88	Cordell	375	120	
AT	4,789,796	12/06/88	Foss	307	443	
AU	4,818,995	04/04/89	Takahashi et al.	341	94	
AV	4,893,087	01/09/90	Davis	328	14	
AW	4,902,986	02/20/90	Lesmeister	331	25	
AX	4,953,128	08/28/90	Kawai et al.	365	194	
AY	4,958,088	09/18/90	Farah-Bakhsh et al.	307	443	
AZ	4,972,470	11/20/90	Farago	380	3	

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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	BA	4,984,204	01/08/91	Sato et al.	365	189.11	
	BB	4,984,255	01/08/91	Davis et al.	375	106	
	BC	5,020,023	05/28/91	Smith	364	900	
	BD	5,038,115	08/06/91	Myers et al.	331	2	
	BE	5,062,082	10/29/91	Choi	365	230.06	
	BF	5,075,569	12/24/91	Branson	307	270	
	BG	5,086,500	02/04/92	Greub	395	550	
	BH	5,087,828	02/11/92	Sato et al.	307	269	
	BI	5,120,990	06/09/92	Koker	307	269	
	BJ	5,122,690	06/16/92	Bianchi	307	475	
	BK	5,128,560	07/07/92	Chern et al.	307	475	
	BL	5,128,563	07/07/92	Hush et al.	307	482	
	BM	5,130,565	07/14/92	Girmay	307	265	
	BN	5,134,311	07/28/92	Biber et al.	307	270	
	BO	5,150,186	09/22/92	Pinney et al.	357	42	
	BP	5,165,046	11/17/92	Hesson	307	270	
	BQ	5,168,199	12/01/92	Huffman et al.	315	370	
	BR	5,179,298	01/12/93	Hirano et al.	307	443	
	BS	5,182,524	01/26/93	Hopkins	330	149	
	BT	5,194,765	03/16/93	Dunlop et al.	307	443	
	BU	5,212,601	05/18/93	Wilson	360	51	
	BV	5,220,208	06/15/93	Schenck	307	443	
	BW	5,223,755	06/29/93	Richley	307	603	
	BX	5,229,929	07/20/93	Shimizu et al.	363	98	
	BY	5,233,314	08/03/93	McDermott et al.	331	17	
	BZ	5,233,564	08/03/93	Ohshima et al.	365	230.05	

EXAMINER <i>[Signature]</i>	DATE CONSIDERED 12-8-05
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<b>INFORMATION DISCLOSURE STATEMENT</b> (Use several sheets if necessary)		APPLICANTS Ralph James	
		FILING DATE October 27, 2003	GROUP ART UNIT 2186

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CA	5,239,206	08/24/93	Yanai	307	272.2	
CB	5,243,703	09/07/93	Farmwald et al.	395	325	
CC	5,254,883	10/19/93	Horowitz et al.	307	443	
CD	5,256,989	10/26/93	Parker et al.	331	1 A	
CE	5,257,294	10/26/93	Pinto et al.	375	120	
CF	5,268,639	12/07/93	Gasbarro et al.	324	158 R	
CG	5,272,729	12/21/93	Bechade et al.	375	118	
CH	5,274,276	12/28/93	Casper et al.	307	443	
CI	5,276,642	01/04/94	Lee	365	189.04	
CJ	5,278,460	01/11/94	Casper	307	296.5	
CK	5,281,865	01/25/94	Yamashita et al.	307	279	
CL	5,283,631	02/01/94	Koerner et al.	307	451	
CM	5,289,580	02/22/94	Latif et al.	395	275	
CN	5,295,164	03/15/94	Yamamura	375	120	
CO	5,304,952	04/19/94	Quiet et al.	331	1 A	
CP	5,311,481	05/10/94	Casper et al.	365	230.06	
CQ	5,311,483	05/10/94	Takasugi	365	233	
CR	5,313,431	05/17/94	Uruma et al.	365	230.05	
CS	5,315,269	05/24/94	Fujii	331	1A	
CT	5,315,388	05/24/94	Shen et al.	348	718	
CU	5,321,368	06/14/94	Hoelzle	328	63	
CV	5,337,285	08/09/94	Ware et al.	365	227	
CW	5,341,405	08/23/94	Mallard, Jr.	375	120	
CX	5,347,177	09/13/94	Lipp	307	443	
CY	5,347,179	09/13/94	Casper et al.	307	451	
CZ	5,355,391	10/11/94	Horowitz et al.	375	36	

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<b>INFORMATION DISCLOSURE STATEMENT</b> (Include several sheets if necessary)		APPLICANTS Ralph James	
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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DA	5,361,002	11/1/94	Casper	327	530	
DB	5,367,649	11/22/94	Cedar	395	375	
DC	5,379,299	01/03/95	Schwartz	370	108	
DD	5,390,308	02/14/95	Ware et al.	395	400	
DE	5,400,283	03/21/95	Raad	365	203	
DF	5,402,389	03/28/95	Flannagan et al.	365	233	
DG	5,408,640	04/18/95	MacIntyre et al.	395	550	
DH	5,410,263	04/25/95	Waizman	327	141	
DI	5,416,436	05/16/95	Rainard	327	270	
DJ	5,416,909	05/16/95	Long et al.	395	275	
DK	5,420,544	05/30/95	Ishibashi	331	11	
DL	5,424,687	06/13/95	Fukuda	331	11	
DM	5,428,311	06/27/95	McClure	327	276	
DN	5,428,317	06/27/95	Sanchez et al.	331	1 A	
DO	5,430,408	07/04/95	Ovens et al.	327	407	
DP	5,430,676	07/04/95	Ware et al.	365	189.02	
DQ	5,432,823	07/11/95	Gasbarro et al.	375	356	
DR	5,438,545	08/01/95	Sim	365	189.05	
DS	5,440,260	08/08/95	Hayashi et al.	327	278	
DT	5,440,514	08/08/95	Flannagan et al.	365	194	
DU	5,444,667	08/22/95	Obara	365	233	
DV	5,446,696	08/29/95	Ware et al.	365	222	
DW	5,448,193	09/05/95	Baumert et al.	327	156	
DX	5,451,898	09/19/95	Johnson	327	563	
DY	5,457,407	10/10/95	Shu et al.	326	30	
DZ	5,465,076	11/07/95	Yamauchi et al.	331	179	

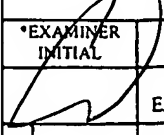
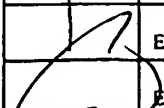
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	EA	5,473,274	12/05/95	Reilly et al.	327	159	
	EB	5,473,575	12/05/95	Farmwald et al.	365	230.06	
	EC	5,473,639	12/05/95	Lee et al.	375	376	
	ED	5,485,490	01/16/96	Leung et al.	375	371	
	EE	5,488,321	01/30/96	Johnson	327	66	
	EF	5,489,864	02/06/96	Ashuri	327	161	
	EG	5,497,127	03/05/96	Sauer	331	17	
	EH	5,497,355	03/05/96	Mills et al.	365	230.08	
	EI	5,498,990	03/12/96	Leung et al.	327	323	
	EJ	5,500,808	03/19/96	Wang	364	578	
	EK	5,502,672	03/26/96	Kwon	365	189.05	
	EL	5,506,814	04/09/96	Hush et al.	365	230.03	
	EM	5,508,638	04/16/96	Cowles et al.	326	38	
	EN	5,513,327	04/30/96	Farmwald et al.	395	309	
	EO	5,515,403	05/07/96	Sloan et al.	375	371	
	EP	5,532,714	07/02/96	Knapp et al.	345	114	
	EQ	5,539,345	07/23/96	Hawkins	327	150	
	ER	5,544,124	08/06/96	Zagar et al.	365	230.08	
	ES	5,544,203	08/06/96	Casasanta et al.	375	376	
	ET	5,550,515	08/27/96	Liang et al.	331	11	
	EU	5,550,783	08/27/96	Stephens, Jr. et al.	365	233	
	EV	5,552,727	09/03/96	Nakao	327	159	
	EW	5,555,429	09/17/96	Parkinson et al.	395	800	
	EX	5,557,224	09/17/96	Wright et al.	327	115	
	EY	5,557,781	09/17/96	Stones et al.	395	550	
	EZ	5,563,546	10/08/96	Tsukada	327	408	

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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
FA	5,568,075	10/22/96	Curran et al.	327	172	
FB	5,568,077	10/22/96	Sato et al.	327	199	
FC	5,572,557	11/05/96	Aoki	375	376	
FD	5,572,722	11/05/96	Vogley	395	555	
FE	5,574,698	11/12/96	Raad	365	230.06	
FF	5,576,645	11/19/96	Farwell	327	94	
FG	5,577,079	11/19/96	Zenno et al.	375	373	
FH	5,577,236	11/19/96	Johnson et al.	395	551	
FI	5,578,940	11/26/96	Dillon et al.	326	30	
FJ	5,578,941	11/26/96	Sher et al.	326	34	
FK	5,579,326	11/26/96	McClure	371	61	
FL	5,581,197	12/03/96	Motley et al.	326	30	
FM	5,589,788	12/31/96	Goto	327	276	
FN	5,590,073	12/31/96	Arakawa et al.	365	185.08	
FO	5,594,690	01/14/97	Rothenberg et al.	365	189.01	
FP	5,614,855	03/25/97	Lee et al.	327	158	
FQ	5,619,473	04/08/97	Hotta	365	238.5	
FR	5,621,340	04/15/97	Lee et al.	327	65	
FS	5,621,690	04/15/97	Jungroth et al.	365	200	
FT	5,621,739	04/15/97	Sine et al.	371	22.1	
FU	5,627,780	05/06/97	Malhi	365	185.09	
FV	5,627,791	05/06/97	Wright et al.	365	222	
FW	5,631,872	05/20/97	Naritake et al.	365	227	
FX	5,636,163	06/03/97	Furutani et al.	365	233	
FY	5,636,173	06/03/97	Schaefer	365	230.03	
FZ	5,636,174	06/03/97	Rao	365	230.03	

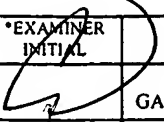
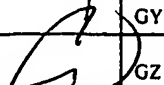
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	GA	5,638,335	06/10/97	Akiyama et al.	365	230.03	
	GB	5,646,904	07/08/97	Ohno et al.	365	233	
	GC	5,652,530	07/29/97	Ashuri	326	93	
	GD	5,657,289	08/12/97	Hush et al.	365	230.05	
	GE	5,657,481	08/12/97	Farmwald et al.	395	551	
	GF	5,663,921	09/02/97	Pascucci et al.	365	233	
	GG	5,666,313	09/09/97	Ichiguchi	365	195	
	GH	5,666,322	09/09/97	Conkle	365	233	
	GI	5,668,763	09/16/97	Fujioka et al.	365	200	
	GJ	5,668,774	09/16/97	Furatani	365	233	
	GK	5,673,005	09/30/97	Pricer	331	1 R	
	GL	5,675,274	10/07/97	Kobayashi et al.	327	158	
	GM	5,675,588	10/07/97	Maruyama et al.	371	20.4	
	GN	5,692,165	11/25/97	Jeddeloh et al.	395	551	
	GO	5,694,065	12/02/97	Hamasaki et al.	327	108	
	GP	5,708,611	01/13/98	Iwamoto	365	233 X	
	GQ	5,712,580	01/27/98	Baumgartner et al.	327	12	
	GR	5,719,508	02/17/98	Daly	327	12	
	GS	5,737,342	04/07/98	Ziperovich	371	25.1	
	GT	5,740,123	04/14/98	Uchida	365	233	
	GU	5,751,665	05/12/98	Tanoi	368	120	
	GV	5,767,715	06/16/98	Marquis et al.	327	159	
	GW	5,768,177	06/16/98	Sakuragi	365	194	
	GX	5,774,699	06/30/98	Nagae	395	551	
	GY	5,778,214	07/07/98	Taya et al.	395	551	
	GZ	5,781,499	07/14/98	Koshikawa	365	233	

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	HA	5,784,422	07/21/98	Heermann	375	355	
	HB	5,789,947	08/04/98	Sato	327	3	
	HC	5,790,612	08/04/98	Chengson et al.	375	373	
	HD	5,794,020	08/11/98	Tanaka et al.	395	552	
	HE	5,805,931	09/08/98	Morzano et al.	395	884	
	HF	5,812,619	09/22/98	Runaldue	375	376	
	HG	5,822,314	10/13/98	Chater-Lea	370	337	
	HH	5,831,929	11/03/98	Manning	365	233	
	HI	5,841,707	11/24/98	Cline et al.	365	194	
	HJ	5,852,378	12/22/98	Keeth	327	171	
	HK	5,872,959	02/16/99	Nguyen et al.	395	552	
	HL	5,889,829	03/30/99	Chiao et al.	375	376	
	HM	5,898,242	04/27/99	Peterson	327	278	
	HN	5,898,674	04/27/99	Mawhinney et al.	370	247	
	HO	5,917,760	06/29/99	Millar	365	194	
	HP	5,920,518	07/06/99	Harrison et al.	365	233	
	HQ	5,926,047	07/20/99	Harrison	327	159	
	HR	5,926,436	07/20/99	Toda et al.	365	236	
	HS	5,940,608	08/17/99	Manning	395	551	
	HT	5,940,609	08/17/99	Harrison	395	559	
	HU	5,945,855	08/31/99	Momtaz	327	157	
	HV	5,946,244	08/31/99	Manning	365	194	
	HW	5,953,284	09/14/99	Baker et al.	365	233	
	HX	5,953,386	09/14/99	Anderson	375	376	
	HY	5,964,884	10/12/99	Partovi et al.	713	503	

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	HZ	5,990,719	11/23/99	Dai et al.	327	244	
	IA	6,005,823	12/21/99	Martin et al.	365	230.08	
	IB	6,011,732	01/04/00	Harrison et al.	365	194	
	IC	6,014,042	01/11/00	Nguyen	327	3	
	ID	6,016,282	01/18/00	Keeth	365	233	
	IE	6,023,489	02/08/00	Hatch	375	208	
	IF	6,026,050	02/15/00	Baker et al.	635	233	
	IG	6,026,134	02/15/00	Duffy et al.	375	376	
	IH	6,029,250	02/22/00	Keeth	713	400	
	II	6,038,219	03/14/00	Mawhinney et al.	370	242	
	IJ	6,067,592	05/23/00	Farmwald et al.	710	104	
	IK	6,087,857	07/11/00	Wang	327	5	
	IL	6,101,152	08/08/00	Farmwald et al.	365	233	
	IM	6,101,197	08/08/00	Keeth et al.	370	517	
	IN	6,105,157	08/15/00	Miller	714	744	
	IO	6,115,318	09/05/00	Keeth	365	233	
	IP	6,147,905	11/14/00	Seino	365	185.11	
	IQ	6,147,916	11/14/00	Ogura	365	203	
	IR	6,160,423	12/12/00	Haq	327	41	
		IS	6,173,432 B1	01/09/01	Harrison	716	1
IT		6,194,917 B1	02/27/01	Deng	327	12	
IU		6,253,360 B1	06/26/01	Yoshiba	716	6	
IV		6,262,921 B1	07/17/01	Manning	365	194	
IW		6,269,451 B1	07/31/01	Mullarkey	713	401	
IX		6,285,726 B1	09/04/01	Gaudet	375	376	
IY		6,295,328 B1	09/25/01	Kim et al.	375	376	
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IZ	6,298,450 B1	10/02/01	Liu et al.	713	502	
JA	6,327,196 B1	12/04/01	Mullarkey	365	194	
JB	6,327,318 B1	12/04/01	Bhullar et al.	375	374	
JC	6,338,127 B1	01/08/02	Manning	711	167	
JD	6,377,646 B1	04/23/02	Sha	375	376	
JE	6,378,079 B1	04/23/02	Mullarkey	713	401	
JF	6,430,696 B1	08/06/02	Keeth	713	503	
JG	6,438,043 B2	08/20/02	Gans et al.	365	194	
JH	6,442,644 B1	08/27/02	Gustavson et al.	711	105	
JI	6,484,244 B1	11/19/02	Manning	711	154	
JJ	6,499,111 B2	12/24/02	Mullarkey	713	401	
JK	6,526,111 B1	02/25/03	Prasad	375	376	
KL	6,665,222 B2	12/16/03	Wright et al.	365	203	

**FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
JM 0 171 720 A2	02/19/86	EP			X	
JN 6-1237512	10/22/86	JP (Abstract Only)			X	
JO 0 295 515 A1	12/21/88	EP			X	
JP 2-112317	4/25/90	JP (+ Abstract)			X	
JQ 0 406 786 A1	1/9/91	EP			X	
JR 0 450 871 A2	10/9/91	EP			X	
JS 0 476 585 A3	3/25/92	EP			X	
JT 4-135311	5/8/92	JP (+ Abstract)			X	
JU 5-136664	6/1/93	JP (+ Abstract)			X	

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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	JV	5-282868	10/29/93	JP (Abstract Only)			X	
	JW	WO 94/29871	12/22/94	PCT			X	
	JX	0 655 741 A2	5/31/95	EP			X	
	JY	0 655 834 A1	5/31/95	EP			X	
	JZ	WO 95/22200	8/17/95	PCT			X	
	KA	WO 95/22206	8/17/95	PCT			X	
	KB	0 680 049 A2	11/2/95	EP			X	
	KC	0-7319577	12/8/95	JP (+ Abstract)			X	
	KD	0 703 663 A1	3/27/96	EP			X	
	KE	0 704 848 A3	4/3/96	EP			X	
	KF	0 704 975 A1	4/3/96	EP			X	
	KG	WO 96/10866	4/11/96	PCT			X	
	KH	0 767 538 A1	4/9/97	EP			X	
	KI	WO 97/14289	4/24/97	PCT			X	
	KJ	WO 97/42557	11/13/97	PCT			X	

### OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	KK	Alvarez, J. et al. "A Wide-Bandwidth Low Voltage PLL for PowerPC <sup>TM</sup> Microprocessors" IEEE IEICE Trans. Electron., Vol. E-78. No. 6, June 1995, pp. 631-639.
	KL	Anonymous, "400MHz SDRAM, 4M X 16 SDRAM Pipelined, Eight Bank, 2.5 V Operation," SDRAM Consortium Advance Sheet, published throughout the United States, pp. 1-22.
	KM	Anonymous, "Draft Standard for a High-Speed Memory Interface (SyncLink)", Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society, Copyright 1996 by the Institute of Electrical and Electronics Engineers, Inc., New York, NY, pp. 1-56.
	KN	Anonymous, "Programmable Pulse Generator", IBM Technical Disclosure Bulletin, Vol. 17, No. 12, May 1975, pp. 3553-3554.



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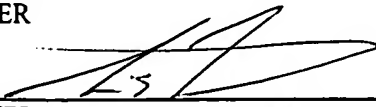
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FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501328.01	APPLICATION NO. 10/695,383
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANTS Ralph James	
		FILING DATE October 27, 2003	GROUP ART UNIT 2186

**OTHER PRIOR ART** (Including Author, Title, Date, Pertinent Pages, Etc.)

	KO	Arai, Y. et al., "A Time Digitizer CMOS Gate-Array with a 250 ps Time Resolution", XP 000597207, IEEE Journal of Solid-State Circuits, Vol. 31, No. 2, February 1996, pp. 212-220.
	KP	Anonymous, "Pulse Combining Network", IBM Technical Disclosure Bulletin, Vol. 32, No. 12, May 1990, pp. 149-151.
	KQ	Anonymous, "Variable Delay Digital Circuit", IBM Technical Disclosure Bulletin, Vol. 35, No. 4A, September 1992, pp. 365-366.
	KR	Arai, Y. et al., "A CMOS Four Channel x 1K Time Memory LSI with 1-ns/b Resolution", IEEE Journal of Solid-State Circuits, Vol. 27, No. 3, M, 8107 March, 1992, No. 3, New York, US, pp. 59-364 and pp. 528-531.
	KS	Aviram, A. et al., "OBTAINING HIGH SPEED PRINTING ON THERMAL SENSITIVE SPECIAL PAPER WITH A RESISTIVE RIBBON PRINT HEAD", IBM Technical Disclosure Bulletin, Vol. 27, No. 5, October 1984, pp. 3059-3060.
	KT	Bazes, M., "Two Novel Fully Complementary Self-Biased CMOS Differential Amplifiers", IEEE Journal of Solid-State Circuits, Vol. 26, No. 2, February 1991, pp. 165-168.
	KU	Chapman, J. et al., "A Low-Cost High-Performance CMOS Timing Vernier for ATE", IEEE International Test Conference, Paper 21.2, 1995, pp. 459-468.
	KV	Cho, J. "Digitally-Controlled PLL with Pulse Width Detection Mechanism for Error Correction", ISSCC 1997, Paper No. SA 20.3, pp. 334-335.
	KW	Christiansen, J., "An Integrated High Resolution CMOS Timing Generator Based on an Array of Delay Locked Loops", IEEE Journal of Solid-State Circuits, Vol. 31, No. 7, July 1996, pp. 952-957.
	KX	Combes, M. et al., "A Portable Clock Multiplier Generator Using Digital CMOS Standard Cells", IEEE Journal of Solid-State Circuits, Vol. 31, No. 7, July 1996, pp. 958-965.
	KY	Donnelly, K. et al., "A 660 MB/s Interface Megacell Portable Circuit in 0.3 $\mu$ m-0.7 $\mu$ m CMOS ASIC", IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996, pp. 1995-2001.
	KZ	Goto, J. et al., "A PLL-Based Programmable Clock Generator with 50- to 350-MHz Oscillating Range for Video Signal Processors", IEICE Trans. Electron., Vol. E77-C, No. 12, December 1994, pp. 1951-1956.
	LA	Gustavson, David B., et al., "IEEE Standard for Scalable Coherent Interface (SCI)," IEEE Computer Society, IEEE Std. 1596-1992, August 2, 1993.
	LB	Hamamoto, T., "400-MHz Random Column Operating SDRAM Techniques with Self-Skew Compensation", IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, May 1998, pp. 770-778.

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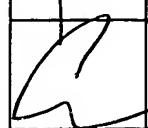
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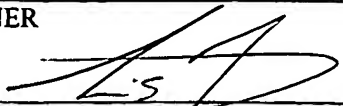
	LC	Ishibashi, A. et al., "High-Speed Clock Distribution Architecture Employing PLL for 0.6μm CMOS SOG", IEEE Custom Integrated Circuits Conference, 1992, pp. 27.6.1-27.6.4.
	LD	Kim, B. et al., "A 30MHz High-Speed Analog/Digital PLL in 2μm CMOS", ISSCC, February 1990.
	LE	Kikuchi, S. et al., "A GATE-ARRAY-BASED 666MHz VLSI TEST SYSTEM", IEEE International Test Conference, Paper 21.1, 1995, pp. 451-458.
	LF	Ko, U. et al., "A 30-ps JITTER, 3.6-μs LOCKING, 3.3-VOLT DIGITAL PLL FOR CMOS GATE ARRAYS", IEEE Custom Integrated Circuits Conference, 1993, pp. 23.3.1-23.3.4.
	LG	Lee, T. et al., "A 2.5V Delay-Locked Loop for an 18Mb 500MB/s DRAM", IEEE International Solid-State Circuits Conference Digest of Technical Papers, Paper No. FA 18.6, 1994, pp. 300-301.
	LH	Lesmeister, G., "A DENSELY INTEGRATED HIGH PERFORMANCE CMOS TESTER", International Test Conference, Paper 16.2, 1991, pp. 426-429.
	LI	Ljuslin, C. et al., "An Integrated 16-channel CMOS Time to Digital Converter", IEEE Nuclear Science Symposium & Medical Imaging Conference Record, Vol. 1, 1993, pp. 625-629.
	LJ	Maneatis, J., "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques", IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1723-1732.
	LK	Nakamura, M. et al., "A 156 Mbps CMOS Clock Recovery Circuit for Burst-mode Transmission", Symposium on VLSI Circuits Digest of Technical Papers, 1996, pp. 122-123.
	LL	Nielson, E., "Inverting latches make simple VCO", EDN, June 19, 1997.
	LM	Novof, I. et al., "Fully Integrated CMOS Phase-Locked Loop with 15 to 240 MHz Locking Range and ±50 ps Jitter", IEEE Journal of Solid-State Circuits, Vol. 30, No. 11, November 1995, pp. 1259-1266.
	LN	Saeki, T. et al., "A 2.5-ns Clock Access, 250-MHz, 256-Mb SDRAM with Synchronous Mirror Delay", IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1656-1665.
	LO	Santos, D. et al., "A CMOS Delay Locked Loop And Sub-Nanosecond Time-to-Digital Converter Chip", IEEE Nuclear Science Symposium and Medical Imaging Conference Record, Vol. 1, October 1995, pp. 289-291.
	LP	Shirotori, T. et al., "PLL-based, Impedance Controlled Output Buffer", 1991 Symposium on VLSI Circuits Digest of Technical Papers, pp. 49-50.

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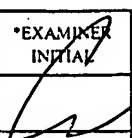
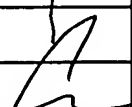
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	LQ	Sidiropoulos, S. et al., "A 700-Mb/s/pin CMOS Signaling Interface Using Current Integrating Receivers", IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997, pp. 681-690.
	LR	Sidiropoulos, S. et al., "A CMOS 500 Mbps/pin synchronous point to point link interface", IEEE Symposium on VLSI Circuits Digest of Technical Papers, 1994, pp. 43-44.
	LS	Sidiropoulos, S. et al., "A Semi-Digital DLL with Unlimited Phase Shift Capability and 0.08-400MHz Operating Range," IEEE International Solid State Circuits Conference, February 8, 1997, pp.332-333.
	LT	Soyuer, M. et al., "A Fully Monolithic 1.25GHz CMOS Frequency Synthesizer", IEEE Symposium on VLSI Circuits Digest of Technical Papers, 1994, pp. 127-128.
	LU	Taguchi, M. et al., "A 40-ns 64-Mb DRAM with 64-b Parallel Data Bus Architecture", IEEE Journal of Solid-State Circuits, Vol. 26, No. 11, November 1991, pp. 1493-1497.
	LV	Tanoi, S. et al., "A 250-622 MHz Deskew and Jitter-Suppressed Clock Buffer Using a Frequency- and Delay-Locked Two-Loop Architecture", 1995 Symposium on VLSI Circuits Digest of Technical Papers, Vol. 11, No. 2, pp. 85-86.
	LW	Tanoi, S. et al., "A 250-622 MHz Deskew and Jitter-Suppressed Clock Buffer Using Two-Loop Architecture", IEEE IEICE Trans. Electron., Vol.E-79-C. No. 7, July 1996, pp.898-904
	LX	von Kaenel, V. et al., "A 320 MHz, 1.5 mW @ 1.35 V CMOS PLL for Microprocessor Clock Generation", IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1715-1722.
	LY	Watson, R. et al., "Clock Buffer Chip with Absolute Delay Regulation Over Process and Environmental Variations", IEEE Custom Integrated Circuits Conference, 1992, pp. 25.2.1-25.2.5.
	LZ	Yoshimura, T. et al. "A 622-Mb/s Bit/Frame Synchronizer for High-Speed Backplane Data Communication", IEEE Journal of Solid-State Circuits, Vol. 31, No. 7, July 1996, pp. 1063-1066.
	MA	
	MB	
	MC	
	MD	

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	APPLICANT(S) <b>Ralph James</b>		
	FILING DATE <b>October 27, 2003</b>	GROUP ART UNIT <b>2186</b>	

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	4,707,823	11/17/87	Holdren et al.	370	1	
	AB	5,241,506	08/31/93	Motegi et al.	365	210	
	AC	5,243,703	09/07/93	Farmwald et al.	395	325	
	AD	5,319,755	06/07/94	Farmwald et a.	395	325	
	AE	5,355,391	10/11/94	Horowitz et al.	375	36	
	AF	5,432,823	07/11/95	Gasbarro et al.	375	356	
	AG	5,465,229	11/07/95	Bechtolsheim et al.	345	477	
	AH	5,502,621	03/26/96	Schumacher et al.	361	760	
	AI	5,566,325	10/15/96	Bruce, II et al.	395	494	
	AJ	5,581,767	12/03/96	Katsuki et al.	395	800	
	AK	5,606,717	02/25/97	Farmwald et al.	395	856	
	AL	5,638,334	06/10/97	Farmwald et al.	365	230.03	
	AM	5,819,304	10/06/98	Nilsen et al.	711	5	
	AN	5,928,343	07/27/99	Farmwald et al.	710	104	
	AO	5,973,935	10/26/99	Schoenfeld et al.	361	813	
	AP	5,973,951	10/26/99	Bechtolsheim et al.	365	52	
	AQ	6,029,250	02/22/00	Keeth	713	400	
	AR	6,061,296	05/09/00	Temullo, Jr., et al.	365	233	
	AS	6,125,431	09/26/00	Kobayashi	711	154	
	AT	6,185,352 B1	02/06/01	Hurley	385	114	
	AU	6,247,107 B1	06/12/01	Christie	711	216	
	AV	6,294,937 B1	09/25/01	Crafts et al.	327	158	
	AW	6,370,068 B2	04/09/02	Rhee	365	196	
	AX	6,401,213 B1	06/04/02	Jeddeloh	713	401	

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	AY	6,430,696 B1	08/06/02	Keeth	713	503	
	AZ	6,434,736 B1	08/13/02	Schaecher et al.	716	17	
	BA	6,479,322 B2	11/12/02	Kawata et al.	438	109	
	BB	6,490,188 B2	12/03/02	Nuxoll et al.	365	63	
	BC	6,646,929 B1	11/11/03	Moss et al.	365	194	
	BD	6,662,304 B2	12/09/03	Keeth et al.	713	400	
	BE	6,667,895 B2	12/23/03	Jang et al.	365	63	
	BF	6,721,195 B2	04/13/04	Brunelle et al.	365	63	
	BG	6,724,685 B2	04/20/04	Braun et al.	365	233	
	BH	6,745,275 B2	06/01/04	Chang	710	305	
	BI	6,751,703 B2	06/15/04	Chilton	711	113	
	BJ	6,756,661 B2	06/29/04	Tsuneda et al.	257	673	
	BK	6,771,538 B2	08/03/04	Shukuri et al.	365	185.05	
	BL	6,792,059 B2	09/14/04	Yuan et al.	375	354	
	BM	6,799,246 B1	09/28/04	Wise et al.	711	117	
	BN	6,804,764 B2	10/12/04	LaBerge et al.	711	170	

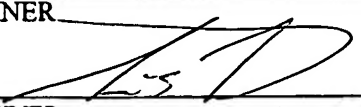
  

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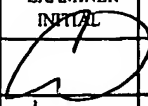

  

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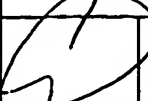
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,544,319	08/06/96	Acton et al.	395	200.07	
	AB	5,822,255	10/13/98	Uchida	365	194	
	AC	5,875,454	02/23/99	Craft et al.	711	113	
	AD	6,033,951	03/07/00	Chao	438	253	
	AE	6,079,008	06/20/00	Clery, III	712	11	
	AF	6,186,400 B1	02/13/01	Dvorkis et al.	235	462.45	
	AG	6,243,831 B1	06/05/01	Mustafa et al.	714	24	
	AH	6,256,692 B1	07/03/01	Yoda et al.	710	104	
	AI	6,392,653 B1	05/21/02	Malandain et al.	345	501	
	AJ	6,438,668 B1	08/20/02	Esfahani et al.	711	165	
	AK	6,636,110 B1	10/21/03	Ooishi et al.	327	565	
	AL	6,681,292 B2	01/20/04	Creta et al.	711	119	
	AM						
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	AU	"Free On-Line Dictionary of Computing" entry 'Flash Erasable Programmable Read-Only Memory, online May 17, 2004 [http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?flash+memory]
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## FOREIGN PATENT DOCUMENTS

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AA	5,818,844	10/06/98	Singh et al.	370	463	
AB	6,272,609	08/07/01	Jeddeloh	711	169	
AC	6,477,592	11/05/02	Chen et al.	710	52	
AD	6,523,092	02/18/03	Fanning	711	134	
AE	6,523,093	02/18/03	Bogin et al.	711	137	
AF	6,622,227	09/16/03	Zumkehr et al.	711	167	
AG	6,631,440	10/07/03	Jenne et al.	711	105	
AH	2002/0144064	10/03/02	Fanning	711	144	
AI	2003/0005223	01/02/03	Coulson et al.	711	118	
AJ	2003/0229770	12/11/03	Jeddeloh	711	213	

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AP	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.
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	AA	5,251,303	10/05/93	Fogg, Jr. et al.	395	275	
	AB	5,432,907	07/11/95	Picazo, Jr. et al.	395	200	
	AC	5,442,770	08/15/95	Barratt	395	403	
	AD	5,497,476	03/05/96	Oldfield et al.	395	439	
	AE	5,659,798	08/19/97	Blumrich et al.	395	846	
	AF	5,875,352	02/23/99	Gentry et al.	395	843	
	AG	6,061,263	05/09/00	Boaz et al.	365	51	
	AH	6,073,190	06/06/00	Rooney	710	56	
	AI	6,105,075	08/15/00	Ghaffari	710	5	
	AJ	6,137,709	10/24/00	Boaz et al.	365	51	
	AK	6,144,587	11/07/00	Yoshida	365	189.05	
	AL	6,167,465	12/26/00	Parvin et al.	710	22	
	AM	6,167,486	12/26/00	Lee et al.	711	120	
	AN	6,201,724 B1	03/13/01	Ishizaki et al.	365	49	
	AO	6,233,376 B1	05/15/01	Updegrove	385	14	
	AP	6,243,769 B1	06/05/01	Rooney	710	56	
	AQ	6,285,349 B1	09/04/01	Smith	345	147	
	AR	6,301,637 B1	10/09/01	Krull et al.	711	112	
	AS	6,327,642 B1	12/04/01	Lee et al.	711	120	
	AT	6,330,205 B2	12/11/01	Shimizu et al.	365	230.06	
	AU	6,347,055 B1	02/12/02	Motomura	365	189.05	
	AV	6,349,363 B2	02/19/02	Cai et al.	711	129	
	AW	6,356,573 B1	03/12/02	Jonsson et al.	372	46	
	AX	6,367,074 B1	04/02/02	Bates et al.	717	11	

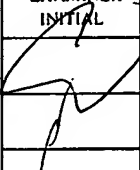
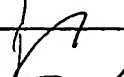
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FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. <b>501328.01</b>	APPLICATION NO. <b>Net yet assigned</b>
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) <b>Ralph James</b>	
		FILING DATE <b>Concurrently herewith</b>	GROUP ART UNIT <b>Not yet assigned</b>

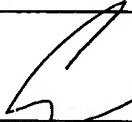
## U.S. PATENT DOCUMENTS

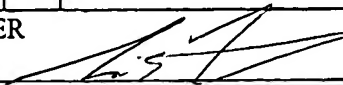
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AY	6,373,777 B1	04/16/02	Suzuki	365	230.03	
	AZ	6,381,190 B1	04/30/02	Shinkai	365	230.03	
	BA	6,421,744 B1	07/16/02	Morrison et al.	710	22	
	BB	6,434,639 B1	08/13/02	Haghighi	710	39	
	BC	6,463,059 B1	10/08/02	Movshovich et al.	370	389	
	BD	6,470,422 B2	10/22/02	Cai et al.	711	129	
	BE	6,473,828 B1	10/29/02	Matsui	711	104	
	BF	6,477,614 B1	11/05/02	Leddige et al.	711	5	
	BG	6,477,621 B1	11/05/02	Lee et al.	711	120	
	BH	6,496,909 B1	12/17/02	Schimmel	711	163	
	BI	6,505,287 B2	01/07/03	Uematsu	711	170	
	BJ	6,539,490 B1	03/25/03	Forbes et al.	713	401	
	BK	6,552,564 B1	04/22/03	Forbes et al.	326	30	
	BL	6,587,912 B2	07/01/03	Leddige et al.	711	5	
	BM	6,594,713 B1	07/15/03	Fuoco et al.	710	31	
	BN	2002/0112119 A1	08/15/02	Halbert et al.	711	115	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	BO							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	BP	Micron Technology, Inc., "Synchronous DRAM Module 512MB/1GB (x72, ECC) 168-PIN Registered FBGA SDRAM DIMM, Micron Technology, Inc., 2002, pp. 1-23.
	BQ	

EXAMINER 	DATE CONSIDERED <b>12-8-05</b>
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